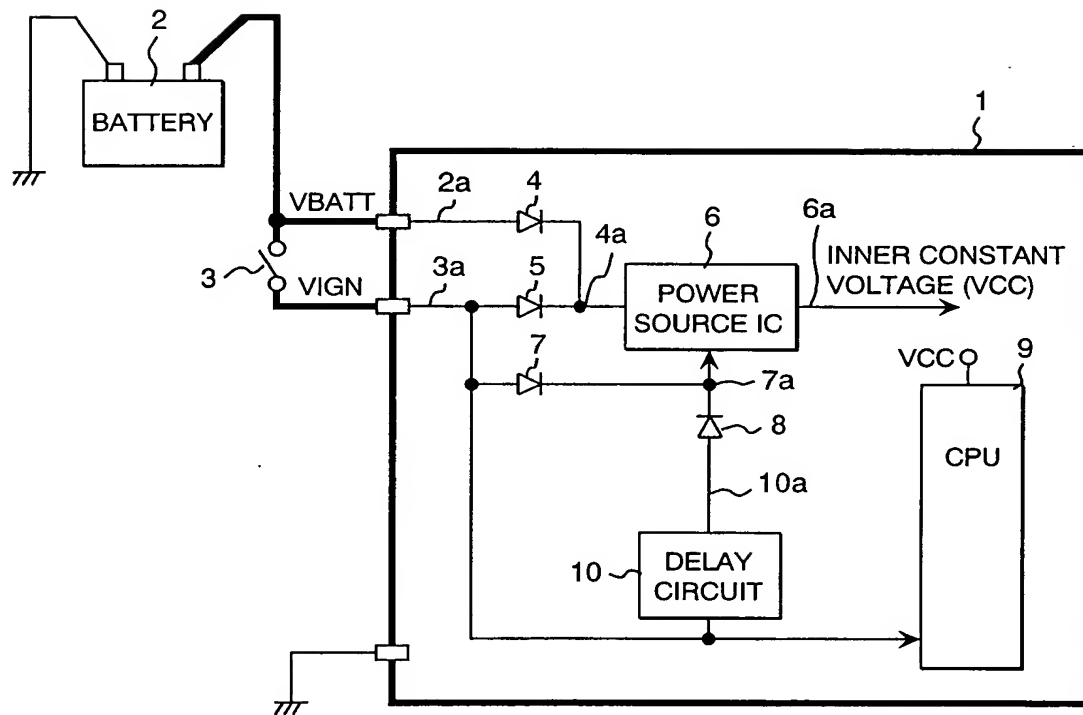
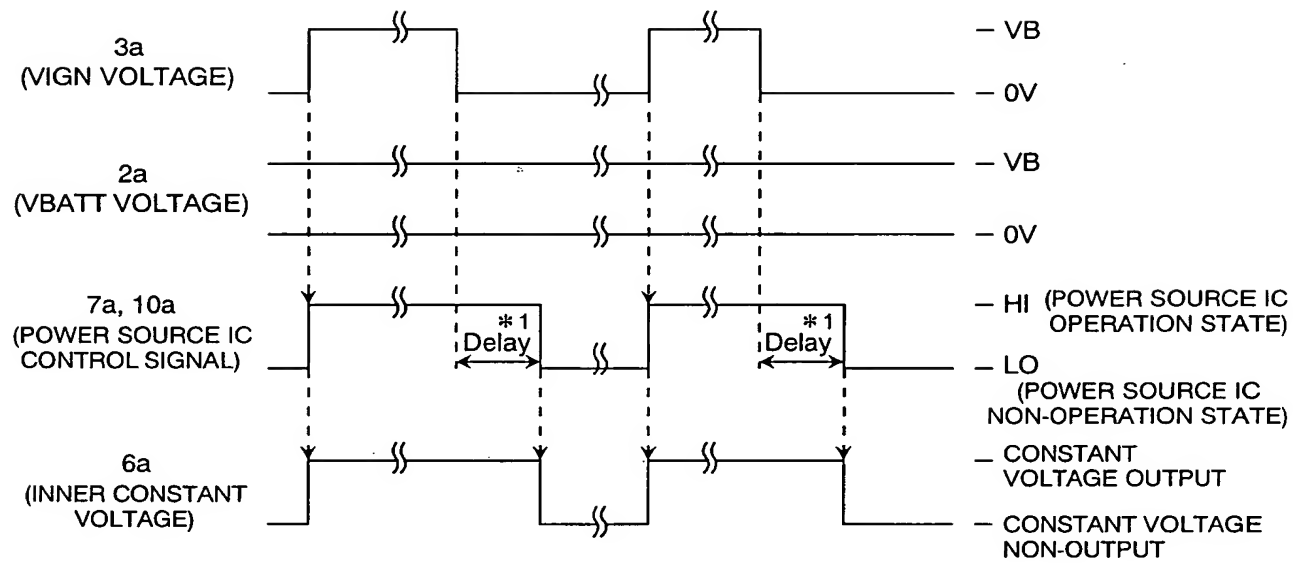
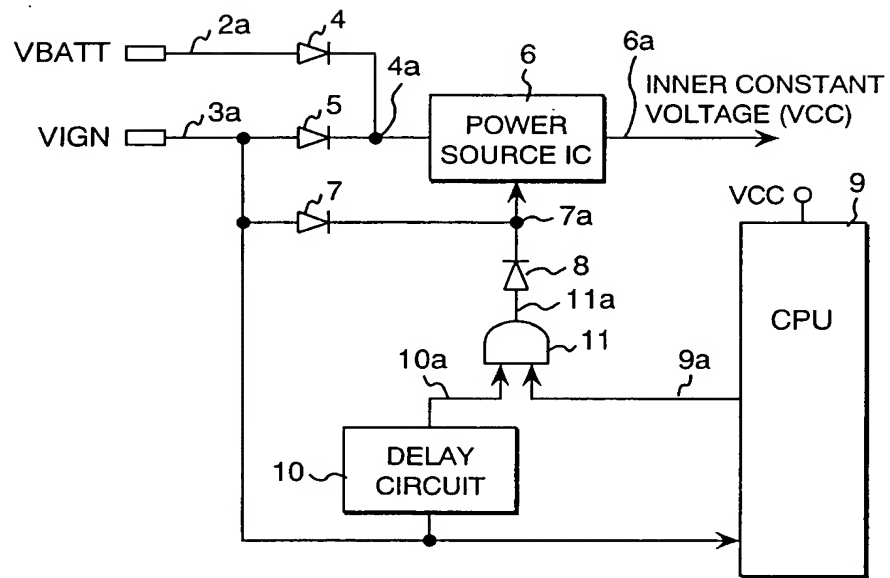
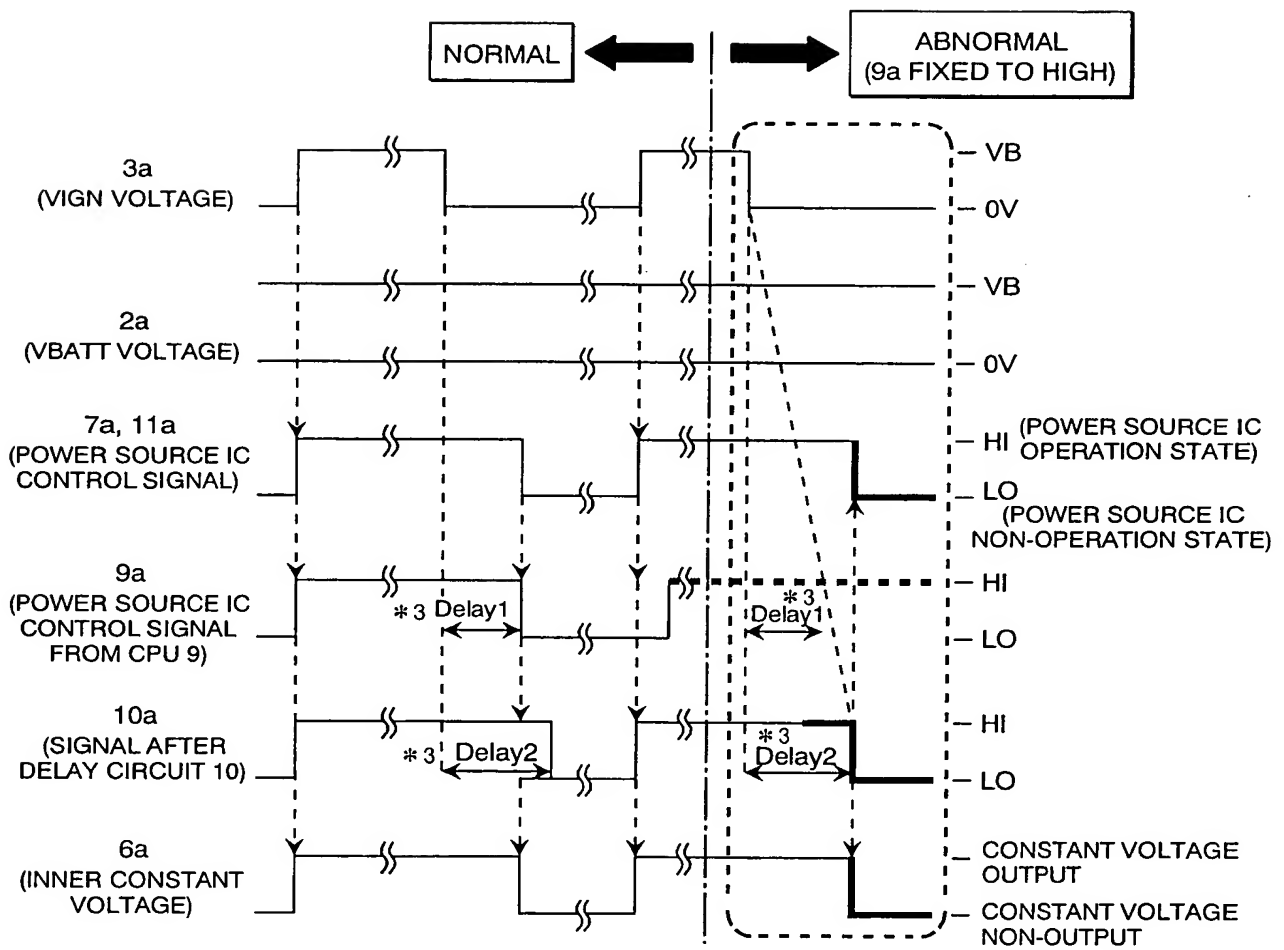


**FIG. 1**

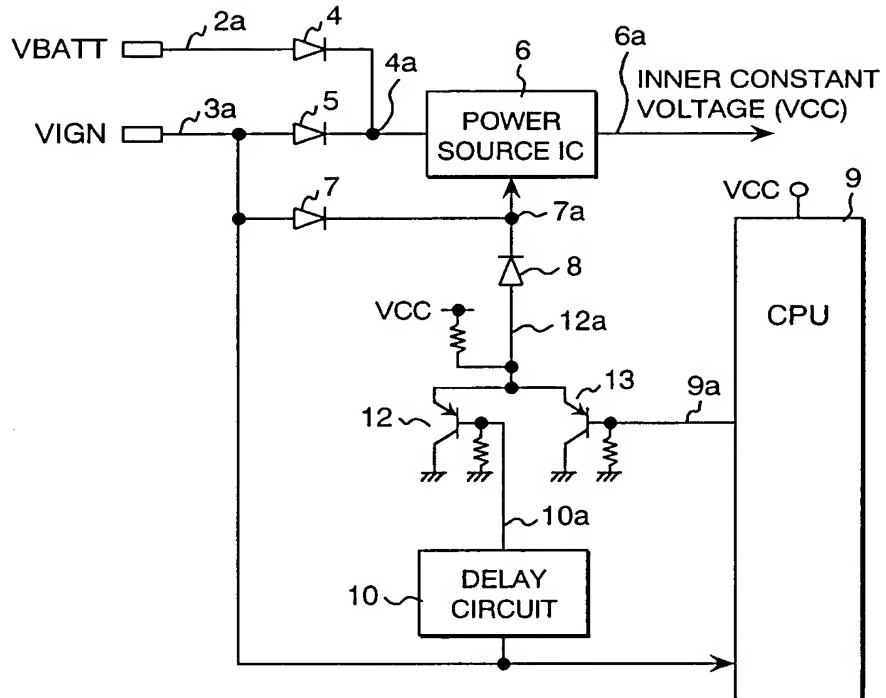
**FIG. 2**

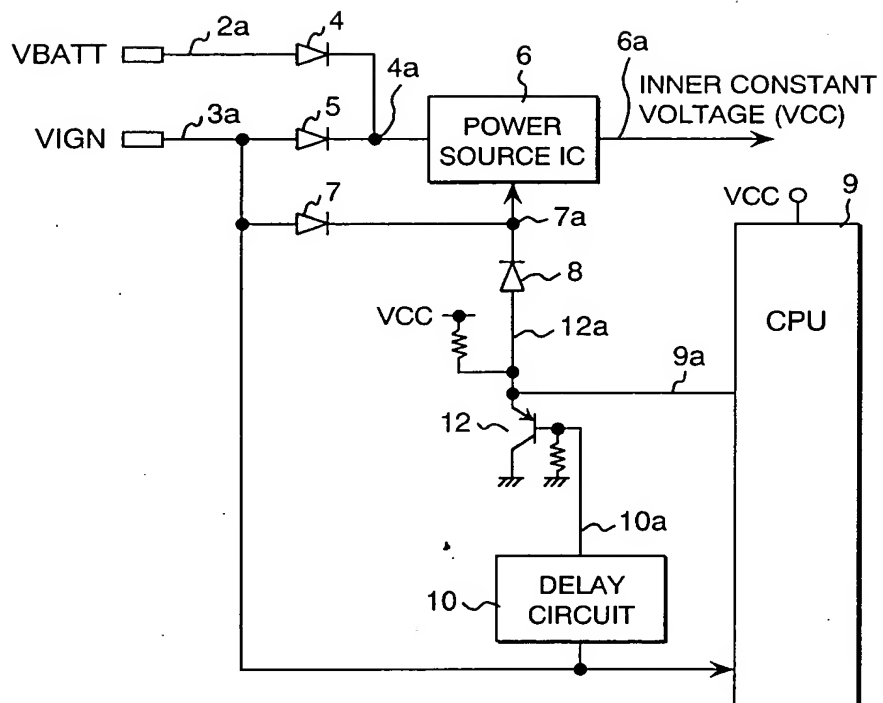
\* 1 : DELAY TIME NECESSARY FOR THE CPU 9 TO INITIALIZE

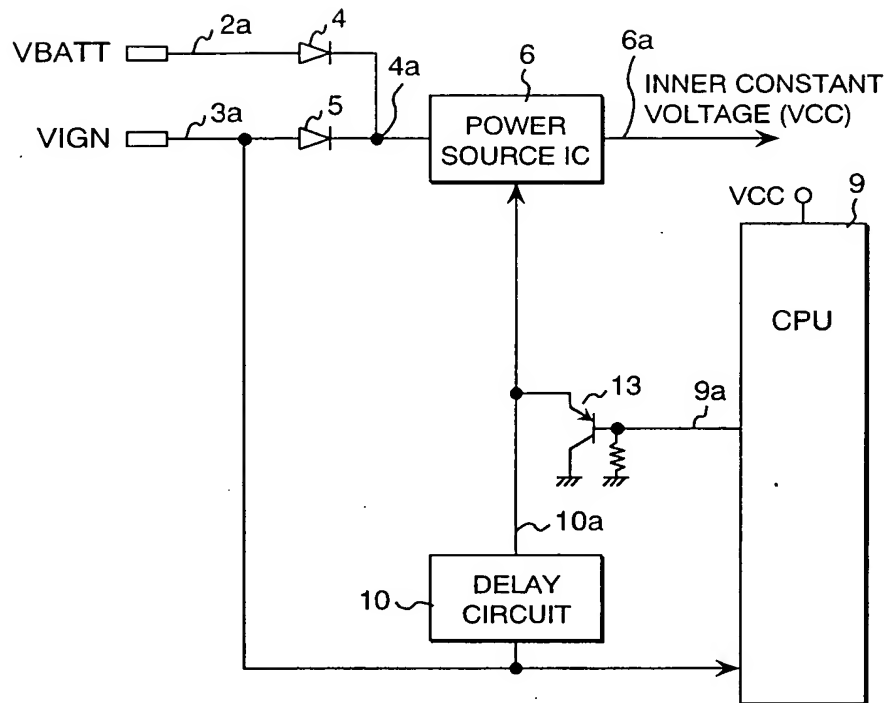
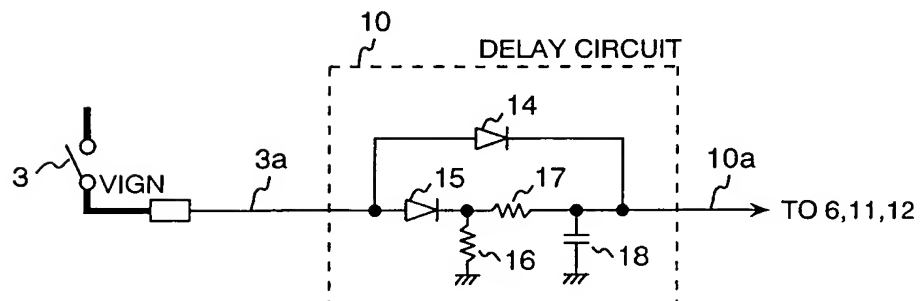
**FIG. 3**

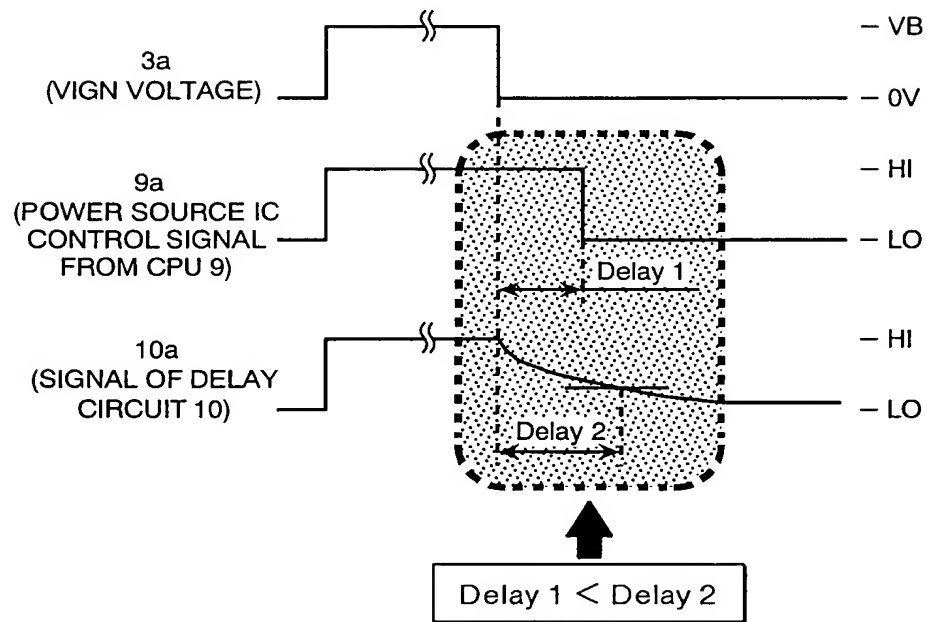
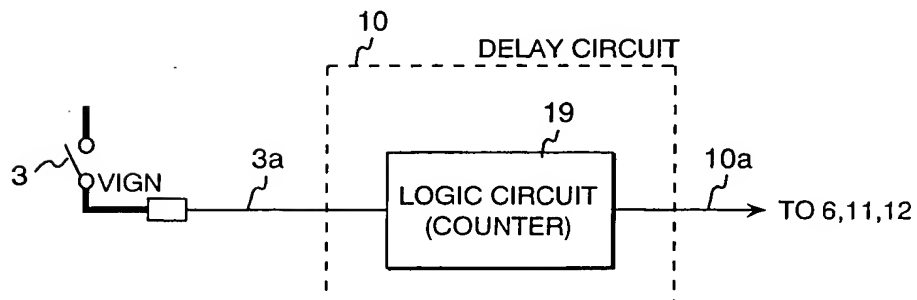
**FIG. 4**

\* 3 DELAY 1: DELAY TIME CALCULATED BY CPU 9  
 DELAY 2: DELAY TIME DUE TO EXTERNAL HARD CIRCUIT

**FIG. 5**

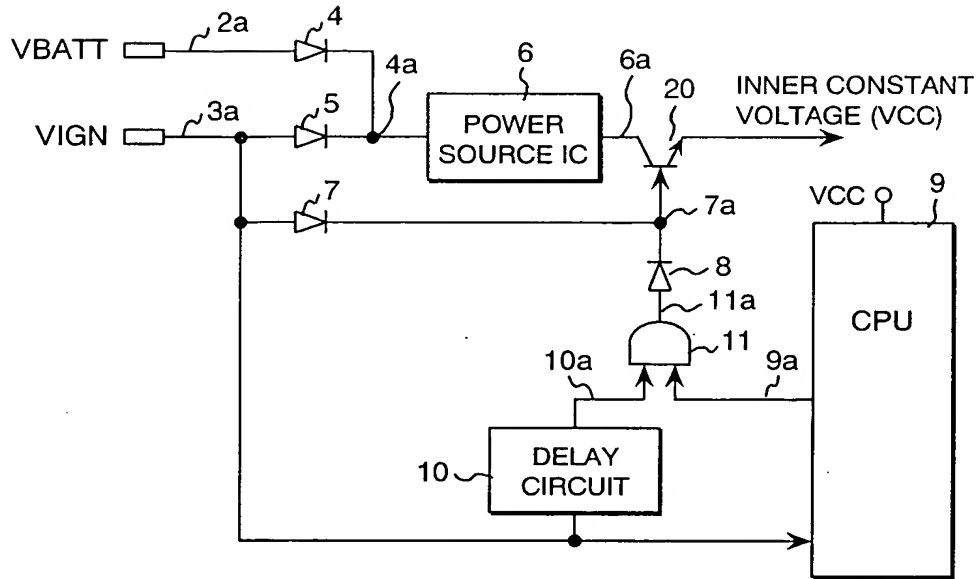
**FIG. 6**

**FIG. 7****FIG. 8**

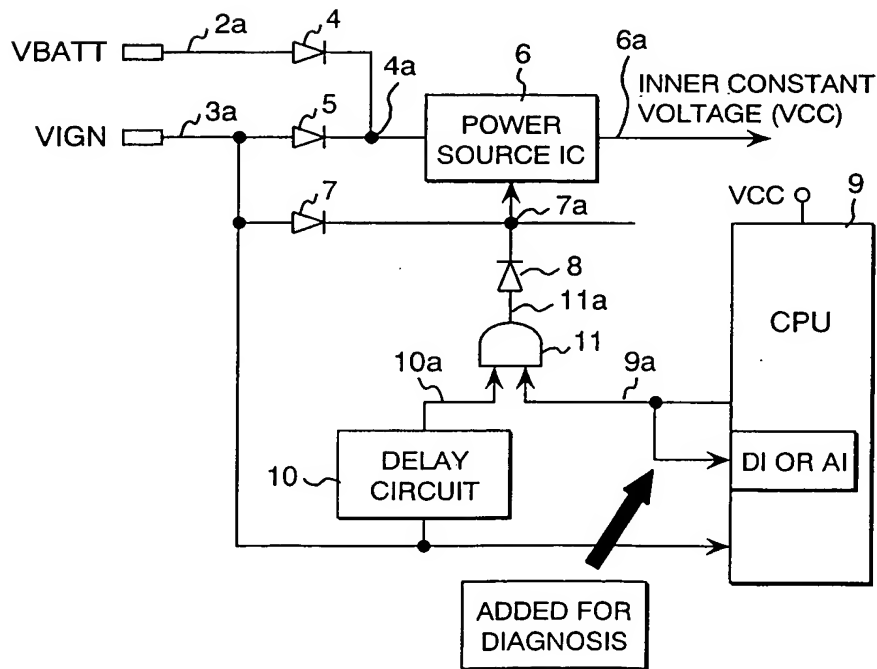
**FIG. 9****FIG. 10**



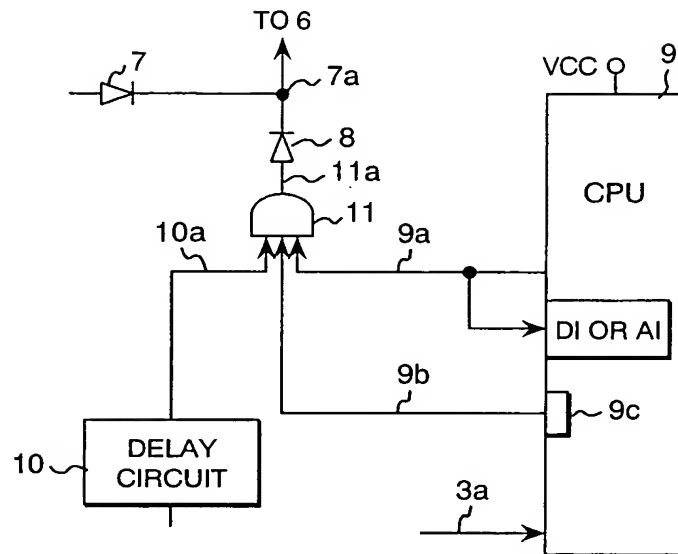
**FIG. 11**



**FIG. 12**

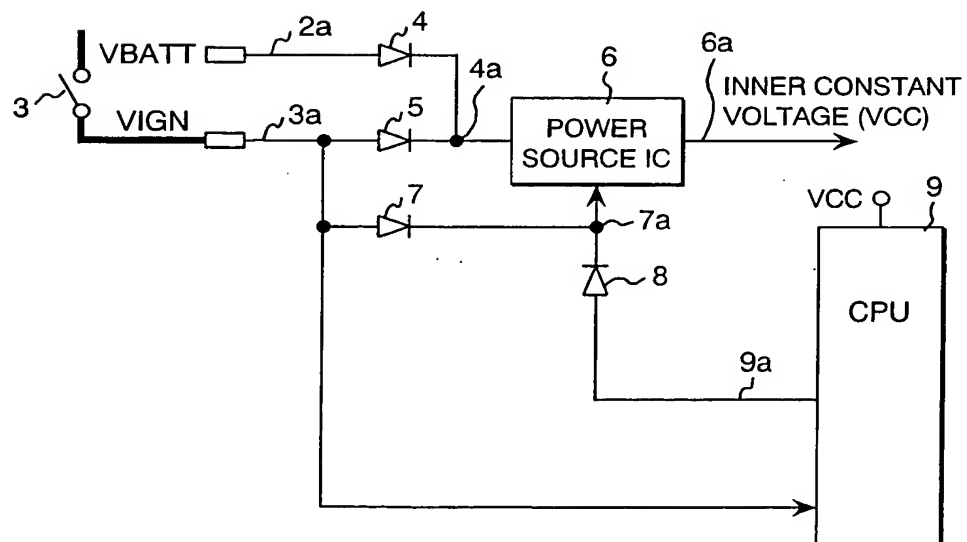


**FIG. 13**



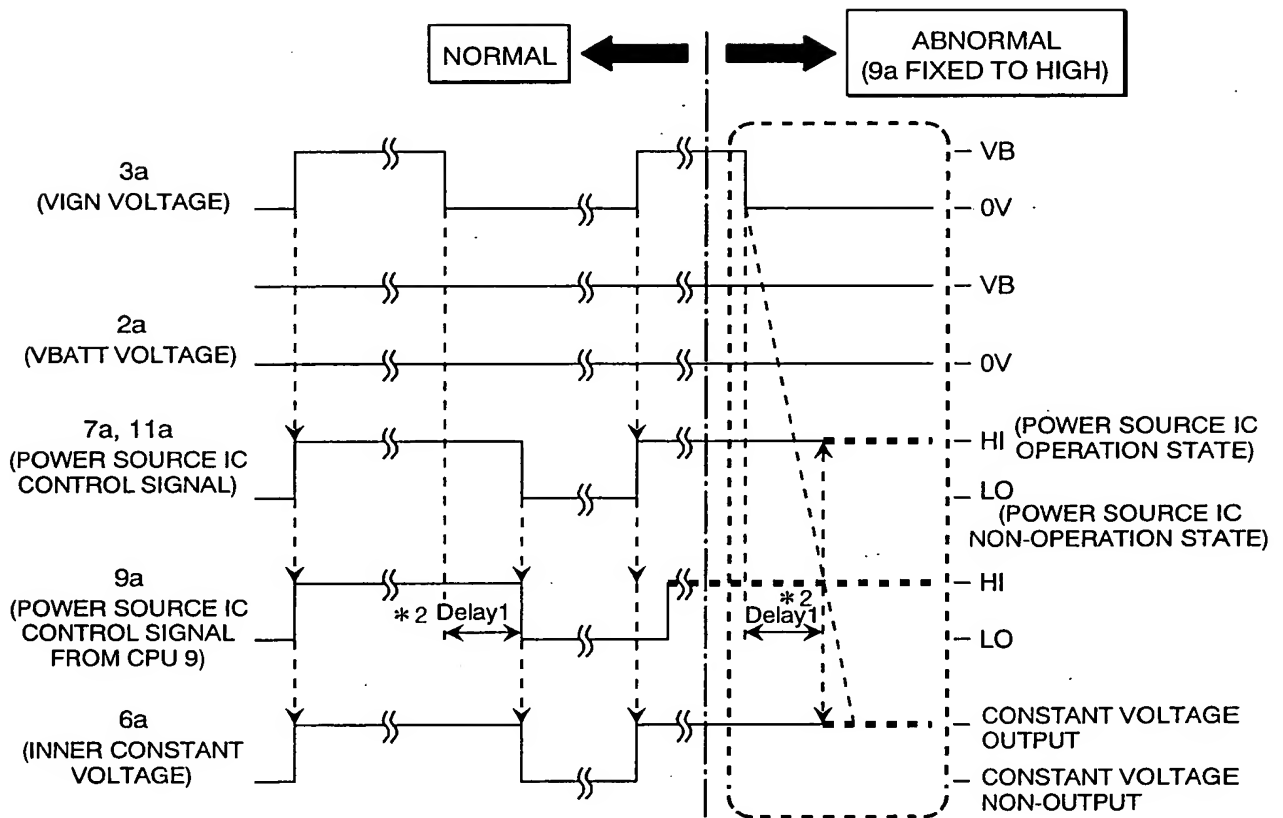
**FIG. 14**

## PRIOR ART



**FIG. 15**

PRIOR ART



\* 2 : DELAY TIME CALCULATED BY CPU 9